

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

THOMAS J. SONDERMAN
PIRAINDER LALL

Serial No.: 10/614,354

Filed: July 7, 2003

For: METHODS OF CONTROLLING
PROPERTIES AND CHARACTERISTICS
OF A GATE INSULATION LAYER BASED
UPON ELECTRICAL TEST DATA, AND
SYSTEM FOR PERFORMING SAME

Confirmation No.: 7900

Group Art Unit: 2823

Examiner: Khiem D. Nguyen

Atty. Dkt. No.: 2000.100800/TT5273

Customer No.: 23720

COMMENTS ON EXAMINER'S STATEMENT OF REASONS FOR ALLOWANCECommissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

With regard to the Statement of Reasons for Allowance, the Examiner made reference to various portions of the language of the pending claims. However, as the Examiner well knows, it is the entirety of the claimed invention that must be considered for purposes of determining patentability. Thus, the Examiner's reference to certain portions of the language of the pending claims should not be understood to somehow limit the presently claimed invention or to give special importance to the language identified by the Examiner. As set forth above, it is the entirety of each of the pending claims that must be considered for purposes of patentability.

Respectfully submitted,

WILLIAMS, MORGAN & AMERSON
CUSTOMER NO. 23720

Date: November 13, 2006

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